

Shree Manibhai Virani & Smt. Navalben Virani Science College, Rajkot
(Autonomous)

Affiliated to Saurashtra University, Rajkot.

BOARD STUDIES - MATHEMATICS

Date: 27.05.2017

Time: 11:00am

Venue: Library reading Room.

MINUTES OF MEETING

AGENDA:

Resolutions are required on

1. To frame syllabus for DSE Allied Mathematics theory & practical courses for Sem-I of B.Sc. Electronics for students admitted from 2017-2018 & onwards.
2. To frame syllabus for DSE Allied 1 Mathematics theory course for Sem.-II of B.Voc. APPLIED COMPUTER TECHNOLOGY for students admitted from 2017-2018 & onwards.
3. Updating of list of paper setters and examiners.
4. Framing of question paper pattern for SEE.
5. Any other matter with permission of the Chair.

BoS Members:-

Sr.No.	Name	Designation	Present / Absent
1.	Mr. Jignasu N. Chauhan	Head of Department, Chairman	
2.	Dr. Samir K.Vaidya	Subject Expert, VC-Nominee	
3.	Dr. Udayan Prajapati	Industry Representative	
4.	Mr. V. N. Vaghasia	Subject Expert, AC nominee	
5.	Mr. A. M. Hirani	Subject Expert, AC nomine	
6.	Mr. Mahesh L. Joshi	Faculty member	
7.	Ms. Bhagvati R. Nabhoya	Faculty member	
8.	Ms. Nisha H. Rabadiya	Faculty member	

9.	Ms. Chandani P. Dhaduk	Faculty member	
10.	Ms. Shweta M. Chitaliya	Faculty member	
11.	Ms. Mital A. Dangar	Faculty member	
12.	Ms. Neha P. Jamvecha	Faculty member	
13.	Dr. Jignesh Bosmiya	Alumni-Academics	

The BoS in Mathematics met as indicated above, and discussed on the above Agenda.

All the members appreciated the material presented to them by the department with respect to agenda. Sharing their expertise with proactive inputs, all the BoS members deliberated on the agenda and unanimously resolved that the agenda as appended are to be recommended to Academic Council for approval for students admitted from **AY 2017-2018 & onwards:**

1. The **Syllabi** framed for the DSE Allied courses offered by Mathematics department for the following programmes.

1.1. B.Sc. Electronics – **Enclosure – I**

1.2. B.Voc. APPLIED COMPUTER TECHNOLOGY– **Enclosure – II**





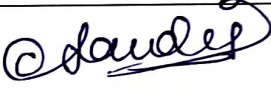
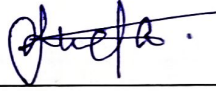


S. No.	Course	Hrs. of Course/ Week	Credit	Offered to	Semester
a.	DSE-Allied: Mathematics for Electronics	3	3	B.Sc. Electronics	I
b.	DSE-Allied 1: Mathematics & Statistics	3	3	B.Voc. APPLIED COMPUTER TECHNOLOGY	II

2. The members unanimously resolved to authorize the Chairperson of the BoS to update the **list of paper setters and examiners.- Enclosure-III**

3. **Question paper pattern for SEE**

3.1. For DSE Allied Mathematics for Electronics theory & practical courses for Sem.-I B.Sc. Electronics adopted as per other B.Sc. programmes **Enclosure – IV**

3.2. Framed for DSE Allied 1 Mathematics for B.Voc. APPLIED COMPUTER TECHNOLOGY theory course for Sem.-II **Enclosure – V**

S.No.	Name of Member	Signature
1.	Mr. Jignasu N. Chauhan (Chairman)	
2.	Dr. Samir K. Vaidya	
3.	Dr. Udayan Prajapati	Leave of Absence
4.	Mr. V. N. Vaghasia	Vaghasia V.N.
5.	Mr. A. M. Hirani	
6.	Mr. Mahesh L. Joshi	Leave of Absence
7.	Ms. Bhagvati R. Nabhoya	Prajapati
8.	Ms. Nisha H. Rabadiya	
9.	Ms. Chandani P. Dhaduk	
10.	Ms. Shweta M. Chitaliya	
11.	Ms. Mital A. Dangar	
12.	Ms. Neha P. Jamvecha	
13.	Dr. Jignesh Bosmiya	